

ATmega wiring scheme for 40/48 pin DIL to QFN64.

GALEP DIL40	Adapter QFN64	ATmega Pin name	Function
1	10	PB0	D0
2	11	PB1	D1
3	12	PB2	D2
4	13	PB3	D3
5	14	PB4	D4
6	15	PB5	D5
7	16	PB6	D6
8	17	PB7	D7
9	20	/RESET	+12V
10	21,52	VCC	
12	24	XTAL1	XTAL1
14	26	PD1	R/B
15	27	PD2	/OE
16	28	PD3	/WR
17	29	PD4	BS1
18	30	PD5	XA0
19	31	PD6	XA1
20	32	PD7	PAGEL
21	22,53,63	GND	
30	64	AVCC	
39	51	PA0	BS2

QFN64

